

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Original)A multistandard RF receiver comprising:
 - a plurality of selectable mixers;
 - a plurality of selectable amplifiers;
 - a configurable analog filter; and
 - a configurable analog to digital converter.
2. (Original)A multistandard RF transmitter comprising:
 - a plurality of selectable mixers;
 - a plurality of selectable amplifiers;
 - a configurable analog filter; and
 - a configurable digital to analog converter.
3. (Cancelled)
4. (Cancelled)
5. (Cancelled)
6. (Currently Amended)A multistandard RF receiver ~~as recited in claim 3~~ comprising:
 - a data interface for sending data to a host controller;
 - a control interface for receiving configuration commands from the host controller
 - wherein the configuration commands identify a wireless standard that is to be implemented by the RF receiver; and
 - an RF processor for processing an RF signal wherein the processed RF signal is output to the host controller on the data interface;
 - wherein the RF receiver includes a configurable signal path.
7. (Cancelled)
8. (Currently Amended)A multistandard RF receiver ~~as recited in claim 3~~ comprising:

a data interface for sending data to a host controller;
a control interface for receiving configuration commands from the host controller
wherein the configuration commands identify a wireless standard that is to be
implemented by the RF receiver; and
an RF processor for processing an RF signal wherein the processed RF signal is
output to the host controller on the data interface;
wherein the RF receiver includes a configurable analog filter.

9. (Currently Amended)A multistandard RF receiver ~~as recited in claim 3~~ comprising:
a data interface for sending data to a host controller;
a control interface for receiving configuration commands from the host controller
wherein the configuration commands identify a wireless standard that is to be
implemented by the RF receiver; and
an RF processor for processing an RF signal wherein the processed RF signal is
output to the host controller on the data interface;
wherein the RF receiver includes configurable analog to digital converter.
10. (Currently Amended)A multistandard RF receiver ~~as recited in claim 3~~ comprising:
a data interface for sending data to a host controller;
a control interface for receiving configuration commands from the host controller
wherein the configuration commands identify a wireless standard that is to be
implemented by the RF receiver; and
an RF processor for processing an RF signal wherein the processed RF signal is
output to the host controller on the data interface;
wherein the RF receiver includes a plurality of selectable mixers.
11. (Currently Amended)A multistandard RF receiver ~~as recited in claim 3~~ comprising:
a data interface for sending data to a host controller;
a control interface for receiving configuration commands from the host controller
wherein the configuration commands identify a wireless standard that is to be
implemented by the RF receiver; and

an RF processor for processing an RF signal wherein the processed RF signal is output to the host controller on the data interface;

wherein the RF receiver includes a plurality of selectable amplifiers.

12. (Cancelled)

13. (Cancelled)

14. (Cancelled)

15. (Currently Amended) ~~A multistandard RF transmitter as recited in claim 12~~ comprising:

a data interface for receiving data from a host controller;

a control interface for receiving configuration commands from the host controller

wherein the configuration commands identify a wireless standard that is to be implemented by the RF transmitter; and

an RF processor for processing an RF signal wherein the processed RF signal is transmitted in accordance with the wireless standard;

wherein the RF transmitter includes a configurable signal path.

16. (Currently Amended) ~~A multistandard RF transmitter as recited in claim 12~~ comprising:

a data interface for receiving data from a host controller;

a control interface for receiving configuration commands from the host controller

wherein the configuration commands identify a wireless standard that is to be implemented by the RF transmitter; and

an RF processor for processing an RF signal wherein the processed RF signal is transmitted in accordance with the wireless standard;

wherein the RF transmitter includes configurable components.

17. (Currently Amended) ~~A multistandard RF transmitter as recited in claim 12~~ comprising:

a data interface for receiving data from a host controller;

a control interface for receiving configuration commands from the host controller

wherein the configuration commands identify a wireless standard that is to be implemented by the RF transmitter; and

an RF processor for processing an RF signal wherein the processed RF signal is transmitted in accordance with the wireless standard;

wherein the RF transmitter includes a configurable analog filter.

18. (Currently Amended)A multistandard RF transmitter ~~as recited in claim 12~~ comprising:

a data interface for receiving data from a host controller;

a control interface for receiving configuration commands from the host controller

wherein the configuration commands identify a wireless standard that is to be implemented by the RF transmitter; and

an RF processor for processing an RF signal wherein the processed RF signal is transmitted in accordance with the wireless standard;

wherein the RF transmitter includes configurable digital to analog converter.

19. (Currently Amended)A multistandard RF transmitter ~~as recited in claim 12~~ comprising:

a data interface for receiving data from a host controller;

a control interface for receiving configuration commands from the host controller

wherein the configuration commands identify a wireless standard that is to be implemented by the RF transmitter; and

an RF processor for processing an RF signal wherein the processed RF signal is transmitted in accordance with the wireless standard;

wherein the RF transmitter includes a plurality of selectable mixers.

20. (Currently Amended)A multistandard RF transmitter ~~as recited in claim 12~~ comprising:

a data interface for receiving data from a host controller;

a control interface for receiving configuration commands from the host controller

wherein the configuration commands identify a wireless standard that is to be implemented by the RF transmitter; and

an RF processor for processing an RF signal wherein the processed RF signal is transmitted in accordance with the wireless standard;

wherein the RF transmitter includes a plurality of selectable amplifiers.

21. (Original)A multistandard RF transceiver comprising:

a plurality of selectable mixers;
a plurality of selectable amplifiers; and
a configurable digital signal processor that is configured to process a received signal or a signal that is being transmitted.

22. (Original)A method of processing an RF signal including:

receiving configuration information from a host processor;
configuring a signal path in accordance with the configuration information; and
configuring components in accordance with the configuration information.

23. (Cancelled)

24. (Currently Amended)A host processor for interacting with a multistandard RF transceiver ~~as recited in claim 23~~ comprising:

a data interface for transmitting data to and receiving data from the multistandard RF transceiver; and

a control interface for sending configuration information to the multistandard RF transceiver;

wherein the host processor receives information from the multistandard RF transceiver indicating the standards that are supported by the multistandard RF transceiver for interacting with a host processor.

25. (Original)A multistandard RF receiver comprising:

an RF processor for processing an RF signal;
a digital to analog converter for converting the processed RF signal to a digital signal;
a digital signal processor for manipulating the digital signal; and
a programmable interface for outputting the processed signal.

26. (Original)A multistandard RF receiver as recited in claim 25 wherein the programmable interface includes a digital to analog converter for generating an analog signal.

27. (Original) A multistandard RF receiver as recited in claim 25 wherein the programmable interface includes a programmable PLL.
28. (Original) A multistandard RF transmitter comprising:
- a programmable interface for receiving a signal to be transmitted;
 - a digital signal processor for outputting a processed signal from the programmable interface for transmitting;
 - an analog to digital converter for converting the processed signal to an analog signal; and
 - an RF processor for processing the analog signal.
29. (Original) A multistandard RF transmitter as recited in claim 28 wherein the programmable interface includes an analog to digital converter for converting the signal to be transmitted to a digital signal.
30. (Original) A multistandard RF transmitter as recited in claim 28 wherein the programmable interface includes a programmable PLL.
31. (Cancelled)
32. (Currently Amended) A multistandard RF transceiver ~~as recited in claim 31~~ comprising:
- a modular RF interface;
 - a control database having parameters for programming the RF interface;
 - a programming interface configured to receive updates for the control database;
 - wherein the programming interface is activated by a code that indicates that the control database is to be updated.
33. (Cancelled)

INTERVIEW SUMMARY UNDER 37 cfr §1.133 AND MPEP §713.04

A telephonic interview in the above-referenced case was conducted on 6/15/2005 between Examiner and Applicants' undersigned representative. The Office Action mailed on 5/13/2005 was discussed. Specifically, Examiner clarified that claim 1 is allowed.

Applicants wish to thank the Examiner for his time and attention in this case.